

Please replace the first full paragraph of page 4 with the replacement paragraph set forth below:

The value loaded into the register 30 is supplied to the address input of a lookup table 34 having a 16 bit data output. The lower byte (bits 0-7) of the word returned by the lookup table in response to the count value of the wide pulse corresponds to the duration of the half pulse whereas the upper byte (bits 15-8) corresponds to the duration of the whole pulse. In the case of the count value being 107, the lower byte is decimal 36 and the upper byte is decimal 71.

In the Claims

Please amend Claims 1-9 as follows:

1. (Once Amended) Apparatus for determining nominal pulse duration values in a signal encoded with an AES3 data stream of data pulses, at least some of which comprise one of one bit cells, two bit cells and three bit cells, the apparatus comprising:

a first circuit for measuring duration of each pulse of the signal and providing a sequence of duration values, and

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a second circuit for detecting a maximum duration value from the sequence of duration values provided by the first circuit, the maximum duration value corresponding to duration of *said*, three bit cells, and providing first and second duration values corresponding to *said* one bit cell and *said* two bit cells respectively.

2. (Once Amended) Apparatus according to claim 1, wherein the second circuit comprises a downcounter and a comparator for detecting the maximum duration value and a lookup table for providing the first and second duration values.

3. (Once Amended) Apparatus according to claim 1, further comprising a third circuit for receiving the maximum duration value and the first and second duration values and comparing the measured duration value of each pulse with the maximum duration value, the first duration value and the second duration value and providing a corresponding output.